

客 戶 名 稱:

## 晶訊國際有限公司



# 承 認 書

## SPECIFICATION FOR APPROVAL

# CUSTOMER'S APPROVAL CHOP 客戶確認蓋章 《條件附確認: Approval's condition: 確認日期 Approved date:

確認這制品, 請簽回一套給我司并蓋上貴司的正式印章 KINDLY RETURN A SET WITH YOUR COMPANY'S OFFICIAL STAMP ON APPROVAL OF THIS ITEM

Ceramic P	TC THERMISTOR	
	SLZ50R	
	В	
2	017/12/27	
Prepared By  Haiying Meng 2017/12/27	Checked By  Zheng Xi 2017/12/27	Approved By  Long Zhong 2017/12/27
	Prepared By  Haiying Meng	Prepared By Checked By  Haiying Meng Zheng Xi

	台灣新北市汐止區大同路一段183號12樓 TEL: 886-2-86922121 FAX: 886-2-26483379
Semitel International Ltd.	12F, No.183, Sec.1, Datong Rd., Xizhi Dist., New Taipei City 22145, Taiwan





## 变更记录

## **Revision Record**

版本 Version	变更日期 Revision date	变更項目/Revision for items	变更原因/Reason For Revision
A	2016/4/12	Tolerance +/- 20% New product item	640pcs per bag package released
В	2017/12/27	Add a note in the position L	make the measurement location intuitively understanding

晶 讯 編 號 : SEMITEL'S MODEL NO. :	SI /50R	客 戶 機 型: CUSTOMER'S MODEL NO.:	
版本/VERSION:	В	客戶型號:	
日期/DATE:	2017/12/27		

晶訊國際有限公司 Semitel International Ltd.



## 晶訊國際有限公司 SLZxxR/ series



General SLZ50R

PTCR( Positive Temperature Coefficient of Resistance) is a kind of positive temperature coefficient thermistor, mainly made up of BaTiO<sub>3</sub> ceramic.BaTiO<sub>3</sub> ceramic is a kind of typical ferroelectric material with the resistivity of greater than  $10^{12} \Omega^*$  cm under normal temperature, through semi-conductor doping it has a strong PTC efficacy --- it has extremely low resistance under normal temperature but it experiences a sudden and large

mutation around curite temperature as ambient temperature rises. This is because a barrier layer induced by the

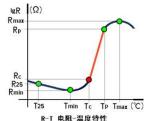


surface state exists on the crystal particle border of the multicrystal BaTiO<sub>3</sub> semiconductor material. Under curie temperature, the high-resistance crystal boundary has ferroelectric characteristics with great dielectric constant and low potential barrier, electrons can easily penetrate the potential barrier and corresponding material have low resistivity. When above Curie temperature, crystal lattice occurs at the high-resistance layer and the ferroelectric constant decreases quickly. The potential barrier increases because the dielectric constant drops in accordance with the Curie-Weiss Law. With the dramatic increase of the barrier height, it becomes difficult for electrons to pass potential barrier and the resistivity of the corresponding materials rises dramatically. This behaves as the PTC efficacy of the material in a

macro manner. Due to this property, the PTC thermistor is widely used both on industrial electronic equipment and household appliances.

The field of its application is classified in light of the three basic electric performances of the PTC.

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## **Description**

SLZxxR series ceramic PTC thermistor is a current protector and specific for Zarlink voice edge SLIC impedance matching. We follow Zarlink's impedance requirement and development all necessary parameters, like resistance, surge response time etc......

Regards the resistance value in normal operation is very low and in abnormal situations like overcurrent or Overheating, will be increased to restrain overcurrent. SLZxxR series can be used for overcurrent protection against current fuse or temperature fuse, due to its ability to return to its initial condition when overcurrent is removed.

## **Application**

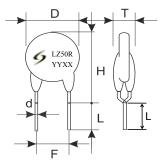
- \* PBX
- \* IP PBX
- \* Telephony
- \* IAD, CO and CPE
- \* EMTA, CO and CPE
- \* Broadband enable voice line card, FXS and FXO
- \* Power resettable fuse





## 1. Structure and Dimensions:

Part No- SLZ50R



#### 1-1.Dimension (unit: mm)

	D	T	Н	F	d	L
Max	9.0	5.0	14.0	5.28	0.65	4.0
Min				4.88	0.55	3.0

1-2 Coating material: Organic Silicon Resin

1-3 Encapsulation color: Grey1-4 Lead type: lateral bending

1-5 Lead material: Tin plating steel wire

## 2. Electrical characteristics:

N0.	Item	Testing condition	Requirement
2.1	Rated zero power resistance R25, $2\Omega$ sub-file	At=25±2 °C, Test instrument accuracy: ± 0.5%, Test current less than 5mA	$50\Omega$ ±42% $2\Omega$ step-file
2.2	Non-trip current Int 25℃	Environment:25 $\pm$ 2°C, supply voltage: $60V_{DC}$ , Electricity flow 75mA, Hold on 1 hour. PTC thermistor should have no significant change $ \Delta R/Rn  \le 50\%$	75mA  ΔR/Rn ≤50%
2.3	Non-trip current Int 40℃	Environment: $40\pm2^{\circ}\text{C}$ , supply voltage: $60\text{V}_{DC}$ , Electricity flow 60mA, Hold on 1 hour. PTC thermistor should have no significant change $ \Delta R/Rn  \le 50\%$	60mA   ΔR/Rn ≤50%
2.4	Trip current It 25°C At = $25\pm2$ °C,220VACrms, electricity flow 150mA,300S into high impedance		150mA≤300S
2.5	Curie temperature (Tc)	The corresponding temperature of twice R25	85°C±7 °C
2.6	Trip Time	Ambient Temperature: $25 \pm 2$ °C , Supply Voltage: 220 VACrms	$3A\rightarrow0.5A<0.15S$ $2A\rightarrow0.5A<0.3S$ $1A\rightarrow0.5A<0.6S$ $0.75A\rightarrow0.15A<1.5S$ $0.5A\rightarrow0.15A<3.5S$ $0.5A\rightarrow0.15A<8S$





2.7	Recovery Time	At=25±2°C, Supply Voltage:220VACrms, Initial current: 1A,Power on 30S. Recovery time is PTC thermistor resistance return to 2 times of rated zero power resistance after power fail test.	≤ 60S
2.8	Resistance to power frequency current capacity	At=25±2 °C, Supply voltage is 220Vrms, Initial current is 3A, power on 1 minute, power fail 10 minutes, Repeat 20 times, . Recovery 4-5 hours under normal temperature and humidity after test. Retest rated zero power resistance.	220Vrms/3A ΔR/Rn≤±20%
2.9	Resistance to power frequency voltage ability	At=25±2 °C, supply voltage :250Vrms, Initial current 3A, Power on 15 minutes, Recovery 4-5 hours under normal temperature and humidity after test. Retest rated zero power resistance.	250Vrms/3A ΔR/Rn≤±20%
2.10	Impact resistance current capacity	At=25±2 °C, Short circuit current waveform:10/1000µs, The minimum open circuit voltage: 1.0KV, short-circuit current peak value 25A, spacing interval: 3 minutes, repeat 30 times.Recovery 4-5 hours under normal temperature and humidity after test. Retest rated zero power resistance.	10/1000μs 1.5KV ΔR/Rn≤±20%
2.11	Induced voltage resistance ability	At=25±2 °C, supply voltage 600VACrms, 600 w series resistance, power on 0.2 second, power failed 1 minute, repeated 5 times. Recovery 4-5 hours under normal temperature and humidity after test. Retest rated zero power resistance.	ΔR/Rn ≤20%
2.12	Failure Mode	Ambient temperature: $25 \pm 2$ °C,Supply voltage: $250\text{VACrms.Series}$ resistance $10\Omega$ ,Duration 30 minutes. Repeat 1 time. Recovery 4-5 hours under normal temperature and humidity after test. Retest rated zero power resistance.	250Vrms/10A, 30 minutes,Allow open circuit or high impedance. No allow burn and flashover.





## 3. Reliability:

3. KC	паршіу:		
NO	Item	Requirement	Test measurement
3.1	Appearance	No visible damage, clear marking	Visual testing
3.2	Solder ability	At least 95% of terminal electrode covered by new solder	Refer to IEC68-2-20(GB2423.28) Ta Dip each lead into 230±5°C solder for 3~5sec to the 5mm above body
3.3	Resistance to solder heat	ΔR/Rn   ≤20% No damage observed	Refer to IEC68-2-20(GB2423.28) Tb, Dip each lead into 260±5°C solder for 5±1sec to the 3mm above body  Recheck Rated zero power resistance after 4-5 hours recovery under normal temperature and humidity condition.
3.4	Tensile strength of terminal	$ \Delta R/Rn  \le 20\%$ No damage observed	Refer to IEC68-2-21(GB2423.29) Test Ua: Pull force10N, for 10 sec. Test Ub: Bending 90 <sup>0</sup> , pull force 5N, twice Test Uc: Rotating 180 <sup>0</sup> , twice. Recheck Rated zero power resistance after 4-5 hours recovery under normal temperature and humidity condition.
3.5	vibration	No damage observed The change rate of resistance $ \Delta R/Rn  \le 20\%$	The samples fixed on the test plate, From 10 hz to 55 hz, displacement amplitude is 0.75 mm within 1 minute. Along the test sample X and Y directions each vibration 45 minutes. Retest Rated zero power resistance after 4-5 hours recovery under normal temperature and humidity condition.
3.6	Collision	No damage observed The change rate of resistance   ΔR/Rn   ≤20%	The samples fixed on the test plate, acceleration is 100 m/s², duration of 11ms, frequency: 60-80 times/minute. Along the test sample X Y two direction the collision 1000 times. Retest Rated zero power resistance after 4-5 hours recovery under normal temperature and humidity condition.
3.7	Steady damp-heat	No damage observed The change rate of resistance    ΔR/Rn   ≤20%	Ambient Temperature: 40°C±2°C Humidity: 90%-95% Duration: 48h No load voltage Retest Rated zero power resistance after 4-5 hours recovery under normal temperature and humidity condition.





3.8	High temperature	No damage observed The change rate of resistance    ∆R/Rn   ≤20%	According to GB/T2423.2 Ba test, Temperature is 70°C, Duration 2 hours, Retest Rated zero power resistance after 4-5 hours recovery under normal temperature and humidity condition.
3.9	Low Temperature	No damage observed The change rate of resistance    ∆R/Rn   ≤20%	According to GB/T2423.2 Ab test, Temperature is -40°C, Duration 2 hours., Retest Rated zero power resistance after 4-5 hours recovery under normal temperature and humidity condition.
3.1	Temperature Variation	No damage observed The change rate of resistance    ∆R/Rn   ≤20%	According to GB/T2423.22 Na test, low temperature is -40°C, high temperature is 70°C, Exposure time is 30min, transfer time is 2min, repeat 5 times; Retest Rated zero power resistance after 4-5 hours recovery under normal temperature and humidity condition.

## 4. Standard Packaging:

■ Bulk: 640 PCS / bag/box

■ Carton: \_\_\_\_\_16 \_\_\_\_ (bag)box / per Carton

640pcs, 20hm step pairing in a bag and in a white box 16 boxes in a carton.

#### Size of the Bag:



Size of the Box:



Size of the Carton:







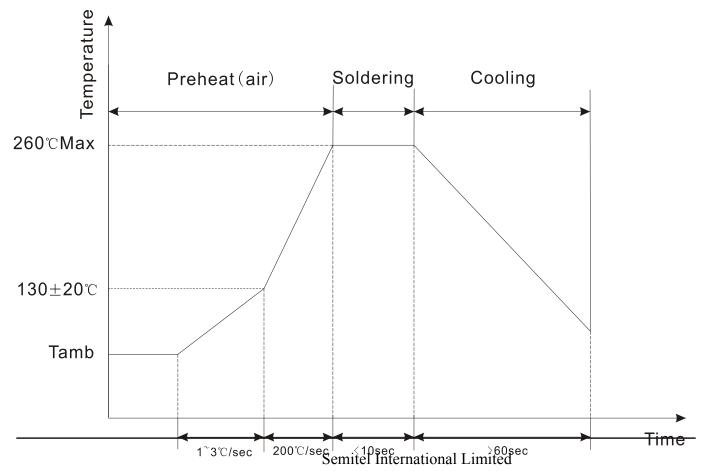
**5.Storage Condition of Products:** 

	1
Storage temperature	-40∼85℃
Relative humidity	≤95%HR (+40°C)
Barometric pressure	86 ∼106Kpa
Vibration frequency	10 Hz ∼50Hz~10 Hz
Collision	$100 \mathrm{m/s^2}$ : $16 \mathrm{ms}$
Other requirement	No acid, alkali and reducing atmosphere

6. Recommended Reworking Conditions With Soldering Iron:

Welding condition	reference craftwork
Soldering iron temperature	360°C (max)
Soldering time	2sec(max)
Distance to coating material	6mm (min)

## 7. Wave Flow Soldering Profile



12F, No 183, Sec 1, Datong Rd, Xizhi City New Taipei City, 22145 Taiwan





8.Inspection

NO.	Item	Inspection level	AQL	Remark
1	Appearance	II	0.65	
2	Size	II	0.65	
3	Rated Zero Power Resistance	II	0.65	
4	Non-Trip Current	S-3	2.5	
5	Trip Current	II	0.65	
6	Trip Time	S-3	2.5	
7	Failure Mode	S-3	2.5	
8	Resistance to high voltage induction ability	S-3	2.5	
9	Solderability	S-3	2.5	
10	Resistance to power frequency current capacity	S-3	2.5	
11	Resistance to power frequency voltage ability	S-3	2.5	
12	Impact resistance current capacity	S-3	2.5	
13	Tensile strength of terminal	S-3	2.5	
14	Curie temperature	-	-	10PCS/per lot, carry out (Ac=1, Re=2) Judge

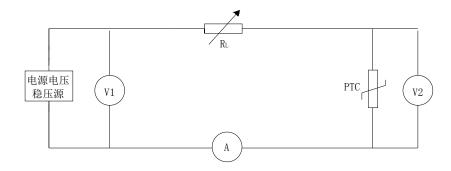
#### 9. Electrical performances tests

- 9-1 Testing of Non-trip Current
- A . Put the PTC in 25°C±2°C for 1H,test the initial resistance  $R_n$  and record;
- B. Refer the 4.24 standard to IEC60738-1 or GB/T 7153, connecting it as fig1 and put into the testing oven, adjust the temperature the same as required and keep the airflow fixed;
- $C_s$  Put the PTC to the constant Voltage, and adjust the  $R_{L_s}$  make the starting Current to the Non-trip Current Value and last as the required time, test the Voltage of PTC and record the data;
- $D_{\times}$  Calculating the Resistance(R) of the PTC by the tested Voltage, calculating it with  $R_{n(the\ starting\ Current\ of\ the\ PTC)}$  the changing Rate of the Resistance ;
- E. PTC could not get the high resistance;
- 9-2 Testing of Trip Current
- A. Put the PTC in  $25^{\circ}\text{C} \pm 2^{\circ}\text{C}$  for 1H,test the initial resistance R<sub>n</sub> and record;



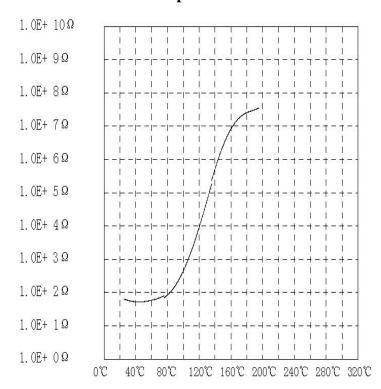


- B. Refer to IEC60738-1 or GB/T 7153 of 4.24, connecting it as fig1 and put into the testing oven; adjust the temperature the same as required and keep the airflow fixed, and keep the PTC under this temperature for 30 minutes;
- C. Put the PTC to the constant Voltage, and adjust the RL, make the starting Current to the Trip Current;
- D. Decreasing the Current to a low Value after a certain time, and that means the PTC get its high resistance; (the Voltage Value is increasing and the reading number of the Ammeter is decreasing, record the data as the reading number decreases half of the starting Current.)



Picture 1: Non-trip and trip current test circuit diagram

#### 10.R/T Resistance-Temperature Curve







#### 11. Attention & Declaration

- 11.1 PTC thermistor is designed under normal environment, so do not use in following environment
- A. Corrosiveness or reducibility gas, (Cl<sub>2</sub>, H<sub>2</sub>S, NH<sub>3</sub>, SO<sub>x</sub>, Noxetc.)
- B, volatile gas
- C. The place which directly contact with water and easy to icing
- D. The place which is put brine, oil, liquid medicine.
- E. The place which vibrates badly
- 11.1.In high impedance state, the temperature itself will surpass 120°C, so confirm whether it has influence to other parts.
- 11.3 It is ceramic product; fall will make the components fracture and damage because of excessive extrusion
- 11.4 This specification approval is to assure the quality of each unit, estimate under the situation when the components are fixed to the customer's products.
- 11.5 PTC thermistor is designed according to the appointed function, so do not violate the rule
- 11.6 If there is any doubtful point, please notice our company asap.
- 11.7 If this specification is confirmed by customer, post back according to the cover address, if not, we will treat that the customer has confirmed the specification.

#### 12. Contact Information

Semitel International Limited 12F, No 183, Sec 1, Datong Rd, Xizhi City New Taipei City, 22145 Taiwan Tel- 886-86922121 Fax-886-26483379 Sales@semitelint.com